

**EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S83	5	"6628467"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:26
S84	4	"662846".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:26
S85	2	"6628467".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:27
S86	73	(read\$4 near2 (amplifier preamplifier)) with (connected loop\$4 circuit loopback feedback connect\$4 multiplexer switch trigger) with (output near2 writ\$4 near2 (amplifier preamplifier))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:35
S87	109	(read\$4 near2 (amplifier preamplifier channel)) with (connected loop\$4 circuit loopback feedback connect\$4 multiplexer switch trigger) with (output near2 writ\$4 near2 (amplifier preamplifier channel))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:36
S88	142	(output near2 (read\$4 reproduc\$4) near2 (amplifier preamplifier circuit)) with (connect S4 loop\$4 loopback feedback multiplexer switch\$4 trigger) with (output near2 (writ\$4 record\$4) near2 (amplifier preamplifier circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:36

S89	114	((read\$4 near2 (amplifier preamplifier)) with (write \$4 near2 (amplifier preamplifier)) and ((test\$4 ((sens\$4 determin\$4 detect\$3 measur\$3 monitor\$3) near4 (error short open fault))) near4 ((read\$4 writ\$4) near2 (driver preamplifier amplifier)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:36
S90	65	((trigger switch multiplex \$4) and (counter register comparator) and S89	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:36
S91	240	((first primary) near3 (register counter)) and (second\$4 near3 (register counter)) and (write near2 amplifier) and (read near2 amplifier)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:39
S92	312	((first primary) near3 (register counter)) with (writ\$4 record\$4)) and (second\$4 near3 (register counter)) and (write near2 (channel preamplifier amplifier)) and (read near2 (channel preamplifier amplifier))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:40
S93	290	((first primary) near3 (register counter)) with (writ\$4 record\$4)) and (second\$4 near3 (register counter)) and (write near2 (channel preamplifier amplifier)) and (read near2 (channel preamplifier amplifier)) and ((connected loop\$4 circuit loopback feedback connect\$4 multiplexer switch trigger) with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:41
S94	631	360/25.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:42
S95	2363	360/31.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:42

S96	1531	360/46.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:42
S97	2538	360/51.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:42
S98	1376	360/53.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:43
S99	3030	360/60-63.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:44
S100	3339	360/66-68.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:44
S101	8088	360/75.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:44
S102	2750	360/69.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:48
S103	1423	710/1.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:48
S104	1097	710/36.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:48
S105	670	710/74.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:48
S106	799	710/72.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:48

S107	521	710/29.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:48
S108	240	710/31.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:49
S109	227	710/34.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:49
S110	681	710/38.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:49
S111	152	710/46.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:49
S112	437	324/210.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:49
S113	400	324/212.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:49
S114	612	324/226.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:49
S115	24	S87 and (S95 S96 S97 S98 S99 S100 S101 S102)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:49
S116	2	S87 and (S103 S104 S105 S106 S107 S108 S109 S110)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:50
S117	2	S87 and (S111 S112 S113 S114)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:50

S118	29	\$93 and (\$95 \$96 \$97 \$98 \$99 \$100 \$101 \$102)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:50
S119	7	\$93 and (\$103 \$104 \$105 \$106 \$107 \$108 \$109 \$110)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:51
S120	1	\$93 and (\$111 \$112 \$113 \$114)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:51
S121	23	\$115 not S118	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:51
S122	28	\$118 not \$115	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:51
S123	1	\$115 and S118	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:52
S124	1965	((read\$4 near2 (amplifier preamplifier channel)) with output) and (connected loop\$4 loopback feedback connect \$4 multiplexer switch trigger) and (output with (writ\$4 near2 (amplifier preamplifier channel))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:57
S125	149	((read\$4 near2 (amplifier preamplifier channel)) with output) and (connected loop\$4 loopback feedback connect \$4 multiplexer switch trigger) and (output with (writ\$4 near2 (amplifier preamplifier channel))).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/22 14:57
S126	46	((read\$4 near2 (amplifier preamplifier channel)) with output) and (connected loop\$4 loopback feedback connect \$4 multiplexer switch trigger) and (output with (writ\$4 near2 (amplifier preamplifier channel))).clm.	US-PGPUB	OR	ON	2008/09/22 14:57

S127	26	((read\$4 near2 amplifier preamplifier)) with output) and (loopback feedback (loop\$4 near2 back) connect\$4 multiplexer switch) and (output with (writ\$4 near2 (amplifier preamplifier))).clm.	US-PGPUB	OR	ON	2008/09/22 15:09
S128	2	((((first primary) near3 (register counter)) with (writ\$4 record\$4)) and (second\$4 near3 (register counter)) and (write near2 (channel preamplifier amplifier)) and (read near2 (channel preamplifier amplifier)) and ((connected loop\$4 circuit loopback feedback connect\$4 multiplexer switch trigger) with signal)). clm.	US-PGPUB	OR	ON	2008/09/22 15:11

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